

Analizador lógico USB muy económico

¿Qué es un analizador lógico?

Referente

<http://vdwaa.nl/logic%20analyzer/pulseview/arch/linux/5-euro-usb-logic-analyzer/>

5 euro USB logic analyzer review

I've found this cheap 5 euro USB logic analyzer via cnx.com and bought it from aliexpress. It turned out to be quite easy to get the analyzer working on Arch Linux, the following packages need to be installed:

```
pacman -S pulseview
```

The fiirmware for the device is only available in the AUR.

```
cower -dd sigrok-firmware-fx2lafw cd sigrok-firmware-fx2lafw && makepkg -si
```

To be able to use the logic analyzer in pulseview without running it as root, requires an udev rule to be setup. Since libsigrok does not provide this udev rule, which might be considered as a packaging bug of Arch Linux.

```
wget http://pkgbuild.com/~jelle/60-libsigrok.rules # As root / sudo cp 60-libsigrok.rules /usr/lib/udev/rules.d/ udevadm control -reload
```

To test the logic analyzer I soldered a simple board which 'taps' the serial communication from the nanopi neo to my laptop. After connecting the ground and RX, TX from the board to the logic analyzer I started pulseview. Then select the saleae logic (or a different name) as device and press run while making some noise in the program connected to the tty device (for example screen). Pulseview 1

In pulseview some peaks should show up, it might help to increase the sample size (for example to 1M). Configuring pulseview to decode UART data is as easy as selecting the UART option from the GUI.

Pulseview 2


Select the connected RX/TX corresponding to how you hooked up them up to the logic analyzer, the baud rate, data bits etc. might be different in your setup.

Pulseview 3

And voila, pulseview decodes the UART data into a readable ascii representation.

Pulseview 4 Summary

This is the first logic analyzer I've ever used and so far it has exceeded my expectations. It was easy to get pulseview working, all the software which is required is fully open source. The analyzer should

be able to analyze I2C, SPI and UART, limited up to 24 MHz. So far worth the 5 euro  Share on

DEBUG USB

<http://devalias.net/devalias/2018/05/13/usb-reverse-engineering-down-the-rabbit-hole/>

From:
<https://wiki.unloquer.org/> -

Permanent link:
https://wiki.unloquer.org/personas/brolin/proyectos/cheap_logic_analyzer

Last update: **2019/12/15 03:31**

